

Dual 210MSPS A/D converter

Description

The AD490 is optimized for high-performance, low-power and ease of use. It is designed for wideband carrier and broadband systems requiring high bandwidth and complex DSP algorithms.

Two 12-bit A/D channel inputs satisfy all rapid data acquisition systems up to 210MSPS.

The AD490 can also implement efficient FFT cores by sharing the algorithm between the two Virtex-4 FPGA devices and the fast onboard memory resources.

The AD490 delivers the highest level of performance and flexibility for real-time data acquisition, processing and recording.

Applications

- ▶ Base station infrastructure
- ▶ Wireless communication receivers
- ▶ Test and measurement instruments
- ▶ Single and multichannel digital receivers
- ▶ High-speed data acquisition systems
- ▶ Radar, infrared sensors
- ▶ Video and imaging cameras
- ▶ Faster scans with quality imaging system

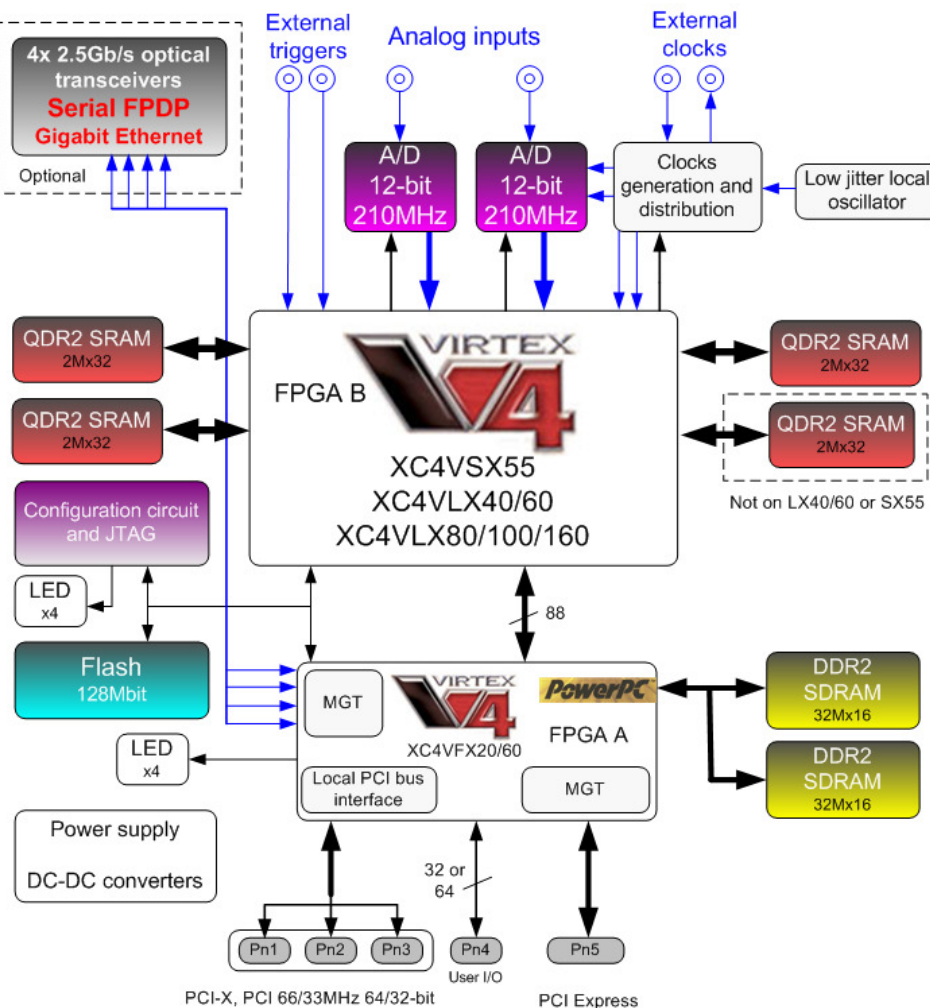
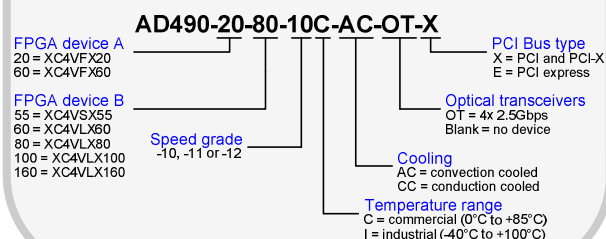
Features

- ▶ Two A/D channels, 210MSPS, 12-bit
- ▶ Virtex-4 FX, LX or SX FPGA architecture
- ▶ Large SRAM and DRAM resources
- ▶ Front panel external I/O expansion options
- ▶ Embedded processor
- ▶ Optimized IP Cores
- ▶ PCI-X 64-bit 133/66MHz
- ▶ Serial FPDP with optical transceivers

Software support

- ▶ Board control and monitoring tools
- ▶ Flash programming utility
- ▶ Confidence tests
- ▶ Host side API
- ▶ Software program example
- ▶ Xilinx ISE projects for both FPGAs
- ▶ Test firmware and VHDL source code
- ▶ Drivers for Windows, Linux or VxWorks

Ordering information



<http://www.4dsp.com/AD490>